



2833 FR

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Michael N. Perugini et al.
Serial No. : 10/280,296
Filed : April 8, 2004
Title : HIGH SPEED, HIGH DENSITY INTERCONNECTION DEVICE

Art Unit : 2833
Examiner : Unknown

Mail Stop Amendment

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Under 35 USC §120, this application relies on the earlier filing date of application serial number 10/178,957, filed on June 24, 2002.

Applicants submit the references listed on the attached form PTO-1449 and PTO-892. However, since the references on the attached list were submitted to and/or cited by the Office in the prior application, they are therefore not provided in this application.

This statement is being filed before the receipt of a first Office action on the merits.

Please apply any charges or credits to Deposit Account No. 06-1050.

Respectfully submitted,

Date: 7/8/04

Stephen L. Romine
Reg. No. 43,056

Fish & Richardson P.C.
225 Franklin Street
Boston, MA 02110-2804
Telephone: (617) 542-5070
Facsimile: (617) 542-8906

20894849.doc

CERTIFICATE OF MAILING BY FIRST CLASS MAIL

I hereby certify under 37 CFR §1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Date of Deposit

July 8, 2004

Signature

Toni M. Sousa

Typed or Printed Name of Person Signing Certificate

Substitute Form PTO-1449
(Modified)U.S. Department of Commerce
Patent and Trademark OfficeAttorney's Docket No.
02021-072001

Application No.

**Information Disclosure Statement
by Applicant**

(Use several sheets if necessary)

Applicant
Michael N. Perugini et al.Filing Date
June 24, 2002

Group Art Unit

(37 CFR §1.98(b))

U.S. Patent Documents

Examiner Initial	Desig. ID	Patent Number	Issue Date	Patentee	Class	Subclass	Filing Date If Appropriate
	AA	3,904,265	9/9/75	Hollyday et al.			
	AB	4,239,318	12/16/80	Schwartz			
	AC	4,449,778	5/22/84	Lane			
	AD	4,718,866	1/12/88	Yamaguchi			
	AE	4,789,357	12/6/88	Yamaguchi et al.			
	AF	4,808,126	2/28/89	Wilson			
	AG	4,898,546	2/6/90	Elco et al.			
	AH	4,909,743	3/20/90	Johnson et al.			
	AI	4,938,704	7/3/90	Fujiura			
	AJ	5,055,069	10/8/91	Townsent et al.			
	AK	5,133,679	7/28/92	Fusselman et al.			
	AL	5,135,405	8/4/92	Fusselman et al.			
	AM	5,256,086	10/26/93	Ponn			
	AN	5,282,268	1/25/94	Mieras et al.			
	AO	5,310,354	5/10/94	Oswald, Jr.			
	AP	5,360,349	11/1/94	Provencher et al.			
	AQ	5,403,206	4/4/95	McNamara et al.			
	AR	5,409,400	4/25/95	Davis			
	AS	5,588,851	12/31/96	Morlion et al.			
	AT	5,672,064	9/30/97	Provencher et al.			
	AU	5,702,255	12/30/97	Murphy et al.			
	AV	5,748,449	5/5/98	Tahmassebpur			
	AW	5,795,191	8/18/98	Preputnick et al.			
	AX	5,993,259	11/30/99	Stokoe et al.			
	AY	5,997,361	12/7/99	Driscoll et al.			
	AZ	6,206,729 B1	3/27/01	Bradley et al.			
	AAA	6,213,787 B1	4/10/01	Murphy			

Examiner Signature

Date Considered

EXAMINER: Initials citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

Substitute Disclosure Form (PTO-1449)



Substitute Form PTO-1449 (Mandatory)	U.S. Department of Commerce Patent and Trademark Office	Attorney's Docket No. 02021-072001	Application No.
Information Disclosure Statement by Applicant (Use several sheets if necessary)		Applicant Michael N. Perugini et al.	
		Filing Date June 24, 2002	Group Art Unit
(37 CFR §1.98(b))			

U.S. Patent Documents							
Examiner Initial	Desig. ID	Patent Number	Issue Date	Patentee	Class	Subclass	Filing Date If Appropriate
	ABB	6,234,827 B1	5/22/01	Nishio et al.			
	ACC	6,281,715	8/28/01	DeClue et al.			

Foreign Patent Documents or Published Foreign Patent Applications								
Examiner Initial	Desig. ID	Document Number	Publication Date	Country or Patent Office	Class	Subclass	Translation	
							Yes	No

Other Documents (include Author, Title, Date, and Place of Publication)		
Examiner Initial	Desig. ID	Document
	ADD	Douglass Brooks, "Differential Impedance: What's the Difference?" <u>Printed Circuit Design</u> , August, 1998, Atlanta, U.S.A.
	AEE	National Semiconductor, <u>LVDS Owners Manual: A General Guide for National's Low Voltage Differential Signalling (LVDS) and Bus LVDS Products</u> , 2 nd ed. Spring, 2000

Examiner Signature	Date Considered
EXAMINER: Initials citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	